



#20/Response  
**PATENT**

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Michael R. Flannery et al.

Examiner: Jose R. Diaz

Serial No.: 09/346,283

Group Art Unit: 2815

Filed: July 1, 1999

Docket: 450.202US1

Title: INTEGRATED CIRCUIT WITH UNIFIED INPUT DEVICE,  
MICROPROCESSOR AND DISPLAY SYSTEMS

10/29/02  
Smith

**RESPONSE UNDER 37 CFR § 1.111**

Commissioner for Patents  
Washington, D.C. 20231

The Applicants' representative has reviewed the Office Action mailed on May 21, 2002. This response is accompanied by a Petition, as well as the appropriate fee, to obtain a two-month extension of the period for responding to the Office action, thereby moving the deadline for response from August 21, 2002 to October 21, 2002. Please consider the appended remarks.

**REMARKS**

The Applicants' representative has carefully reviewed and considered the Office Action mailed on May 21, 2002, and the references cited therewith. No claims are amended, no claims are canceled, and no claims are added. As a result, claims 1-7 are now pending in this Application.

**§102 Rejection of the Claims**

Claims 1 and 7 were rejected under 35 USC § 102(b) as being anticipated by Nair et al. (US 5,362,952, hereinafter "Nair"). However, the MPEP requires that "[t]he identical invention must be shown in as complete detail as is contained in the ... claim." See M.P.E.P. § 2131. Because the Applicants assert that Nair does not disclose the identical invention claimed, this rejection is respectfully traversed.

Nair fails to disclose "[a]n integrated circuit with a micromechanical element comprising a semiconductor support substrate ..." as claimed by the Applicant in amended independent claim 1 (from which claims 2-7 depend). While asserting the existence of such an element in the Office Action (see Office Action, "Claim Rejections - 35 USC § 102", "... comprising a support substrate (48) supporting a sensor element (70) ..."), a careful reading of